

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.
17119

In Re Application Of: **Yoshinori Muramatsu**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/685,030	Oct. 14, 2003	Unassigned	23389	2817	4843

Title: **VOLTAGE CONTROLLED OSCILLATOR**



Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
 - the statement specified in 37 CFR 1.97(e);
 - OR**
 - the fee set forth in 37 CFR 1.17(p).

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10/685,038 P F	Oct. 14, 2003	Unassigned	23389	2817	4843

Title: **VOLTAGE CONTROLLED OSCILLATOR**



Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- A check in the amount of _____ is attached.
- The Director is hereby authorized to charge and credit Deposit Account No. 19-1013/SSMP as described below.
 - Charge the amount of _____
 - Credit any overpayment.
 - Charge any additional fee required.
- Payment by credit card. Form PTO-2038 is attached.

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

April 10, 2006

(Date)

Signature of Person Mailing Correspondence

Paul J. Esatto, Jr.

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*This certificate may only be used if paying by deposit account.

Signature

Paul J. Esatto, Jr., Reg. No. 30,749
 SCULLY, SCOTT, MURPHY & PRESSER, P.C.

Dated: April 10, 2006

PJE:ahs

CC:



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshinori Muramatsu

Examiner: Unassigned

Serial No.: 10/685,030

Art Unit: 2817

Filed: Oct. 14, 2003

Docket: 17119

For: VOLTAGE CONTROLLED OSCILLATOR **Dated:** April 10, 2006

Confirmation No. 4843

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 23313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

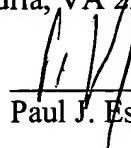
In accordance with 37 C.F.R §§1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Sjoland, H., "Improved Switched Tuning of Differential CMOS VCOs", IEEE Transactions on Circuits and Systems II, Analog and Digital Signal Processing, U.S.A., May 2002, Vol. 49, Issue 5, pp. 352-355;

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 23313-1450 on April 10, 2006.

Dated: April 10, 2006


Paul J. Esatto, Jr.

2. Japanese Unexamined Patent Application No. 11-330852, dated November 30, 1999, together with English-language Abstract;
3. Japanese Unexamined Patent Application No. 2001-203603, dated July 27, 2001, together with English-language Abstract; and

The references were cited in an Office Action dated January 17, 2006 received from the Japanese Patent Office. Applicant is submitting copies of the above-cited references, together with an English translation of the Examiner's comments regarding the references from the Official Action. The relevance of the references is described in the Official Action. Please note that the first listed reference cited in the Office Action, namely, Kral, A. et al., "RF-CMOS Oscillators with Switched Tuning", Custom Integrated Circuits Conference, 1998, Proceedings of the IEEE 1998, U.S.A., May 14, 1998 (pp. 555-558), was previously submitted in applicant's Information Disclosure Statement dated October 14, 2003.

In compliance with the requirements of 37 C.F.R. §1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designated in 37 C.F.R. §1.56(c) most knowledgeable about the content of the information, the undersigned attorney of record submits a translation of portions of an official action by a foreign examiner in which the references were cited. The relevance to the pending U.S. patent application is that the references were cited in a foreign patent application on the same subject matter. However, no independent analysis of the references, the accuracy of the statement of the foreign examiner or the claims of the foreign application under the laws of that country or the United States relative to the subject matter claimed in the present application has been made; the present understanding of the

contents thereof by the undersigned being based on the translation of the foreign examiner's comments submitted herewith.

The undersigned attorney hereby states that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

Respectfully submitted,


Paul J. Esatto, Jr.
Reg. No. 30, 749

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PJE:ahs


INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

17119

Application Number

10/685,030

Applicant(s)

Yoshinori Muramatsu

Filing Date

Oct. 14, 2003

Group Art Unit

2817

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	11-330852	11/30/99	Japan			✓	
	2001-203603	7/27/01	Japan			✓	

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

Sjoland, H., "Improved Switched Tuning of Differential CMOS VCOs", IEEE Transactions on Circuits and Systems II, Analog and Digital Signal Processing, U.S.A., May 2002, Vol. 49, Issue 5, pp. 352-355

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.